Secretary of State Mobile Unit on Campus Today, 10/8, to Register Voters

The Michigan Secretary of State will have its mobile unit on campus in the Michigan League's circular drive off of North University Avenue on Monday, October 8 and will be open for business from 10:00 AM to 4:00 PM. The primary focus of this visit is voter registration. However, the mobile unit is equipped to handle most regular Secretary of State services. If you wish to register to vote, don’t forget your driver’s license or Michigan personal identification card.

Election Day is Tuesday, November 6. If you have not yet registered to vote in Michigan, you have until 5:00 PM on Tuesday, October 9 to register. Voter registration forms and instructions can be accessed via the following websites:

If you are voting in Michigan: Michigan Secretary of State
If you are voting out-of-state: U.S Election Assistance Commission

Additionally, the University of Michigan’s Ginsberg Center is partnering with TurboVote, a quick, customizable tool accessible to all UM faculty, staff, and students. TurboVote can help you register to vote either here in Ann Arbor or nationwide by using your permanent address.

If you plan to vote in the City of Ann Arbor, you can also register to vote in person Monday through Friday between the hours of 8:00 AM and 6:00 PM at the office of the Ann Arbor City Clerk, Second Floor, Ann Arbor City Hall, 301 East Huron Street. If you have questions for the City Clerk’s staff, please call 734-794-6140 and then press 0 to speak with a City Clerk employee.

SWE Amazon Corporate Information Session

Amazon
Monday, 10/8/18 6:30-8:00 PM
EECS 1500
Position: Full-Time, Intern
Majors: All Engineering Majors
Degrees: Bachelors, Masters, PhD
Citizenship: None
Resumes: No

*Food will be provided*

Contact: Society of Women Engineers (swe.cis.publicity@umich.edu)

IEEE Facebook Infrastructure Tabling Event
Monday, October 8th
10:00 AM – 2:00 PM
Duderstadt Atrium

Bring your resume and come talk directly with a Hardware Engineer, Production Engineer, Performance & Capacity Engineer, Infrastructure Data Scientist, and Recruiter!
Title: Material Challenges and Opportunities in Next Generation Electronics: From Non-silicon Electronics to Artificial Neural Networks

Speaker: Prof. Jeehwan Kim

Date: Tuesday, October 9th
Time: 4:00 - 5:00 pm
Location: G906 Cooley

"Refreshments provided"

ABSTRACT: The current electronics industry has been completely dominated by Si-based devices due to its exceptionally low materials cost. However, demand for non-Si electronics is becoming substantially high because current/next generation electronics requires novel functionalities that can never be achieved by Si-based materials. Unfortunately, the extremely high cost of non-Si semiconductor materials prohibits the progress in this field. I will discuss about my group’s efforts to address these issues. Our team has recently conceived a new crystalline growth concept, termed as "remote epitaxy", which can copy/paste crystalline information from the wafer remotely through graphene, thus generating single-crystalline films on graphene [1-2]. These single-crystalline films can be easily released from the slippery graphene surface, and the graphene-coated substrates can be reused infinitely to generate single-crystalline films. Therefore, the remote epitaxy technique can produce expensive non-Si semiconductor films with unprecedented cost efficiency while allowing additional flexible device functionality required for current ubiquitous electronics.

Lastly, I will discuss about an ultimate alternative computing solution that does not follow the conventional von Neuman method. As Moore’s law approaches its physical limits, brain-inspired neuromorphic computing has recently emerged as a promising alternative because of its compatibility with AI. In the neuromorphic computing system, resistive random access memory (RRAM) can be used as an artificial synapse for weight elements in neural network algorithms. RRAM typically utilizes a defect amorphous solid as a switching medium. However, due to the random nature of amorphous phase, it has been challenging to precisely control weights in artificial synapses, thus resulting in poor learning accuracy. Our team recently demonstrated single-crystalline-based artificial synapses that show precise control of synaptic weights, promising superior online learning accuracy of 95.1% – a key step paving the way towards post von Neumann computing [3]. I will discuss about how we design the materials and devices for this new neuromorphic hardware.

BIOGRAPHY: Professor Jeehwan Kim is an Associate Professor of Massachusetts Institute of Technology in the Mechanical engineering and Materials Science and Engineering. He is a Principal Investigator in Research Laboratory of Electronics at MIT. Prof. Kim’s group focuses on innovation in nanotechnology for next generation computing and electronics. Before joining MIT in 2015, he was a Research Staff Member at IBM T.J. Watson Research Center in Yorktown Heights, NY since 2008. Many of his patents have been licensed for commercialization. Prof. Kim is a recipient of 20 IBM high value invention achievement awards. In 2012, he was appointed a "Master Inventor" of IBM in recognition of his active intellectual property generation and commercialization of his research. He is an inventor of 200 issued/pending U.S. patents and an author of 40 articles in journals. He received his BS from Hongik University, his MS from Seoul National University, and his PhD from UCLA in 2008, all of them in Materials Science.
American Express will be on the campus this Thursday, October 11th, hosting Coffee Chats and Corporate Presentations for their Credit and Fraud Risk (CFR) full-time program. During the chats, students will have the opportunity to connect with a Manager in CFR, learn about the business, and the full-time opportunities.

Coffee Chats  
October 11th  
12:30 – 4:30 PM  
Duderstadt Center – 2nd Floor

Undergrad Corporate Presentations  
October 11th  
5:00 – 8:00 PM  
DOW 1005 (2300 Hayward Street)

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IEEE Facebook Infrastructure Panel  
Monday, October 8th  
7:00 PM – 8:00 PM  
EECS 1200

Come to the IEEE Facebook Infrastructure Panel for Potbelly sandwiches and to hear about what problems we are solving directly from a Hardware Engineer, Production Engineer, Performance & Capacity Engineer, and Infrastructure Data Scientist! Click here for more information.

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*This information is sent on behalf of the individuals listed in each announcements. These opportunities are not directly affiliated with the EECS Undergraduate Advising Office.*

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